

# Introduction to OpenMP Device Offload

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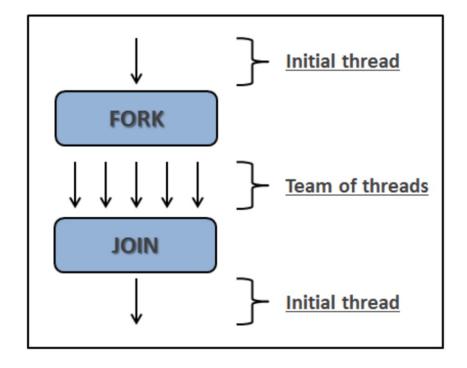
### Outline

- Introduction to OpenMP
- History of OpenMP
- Recap of OpenMP Worksharing
- Introduction to OpenMP Offload
- Offload Steps
- Expressing parallelism
- Useful Runtime Routines
- Hands On

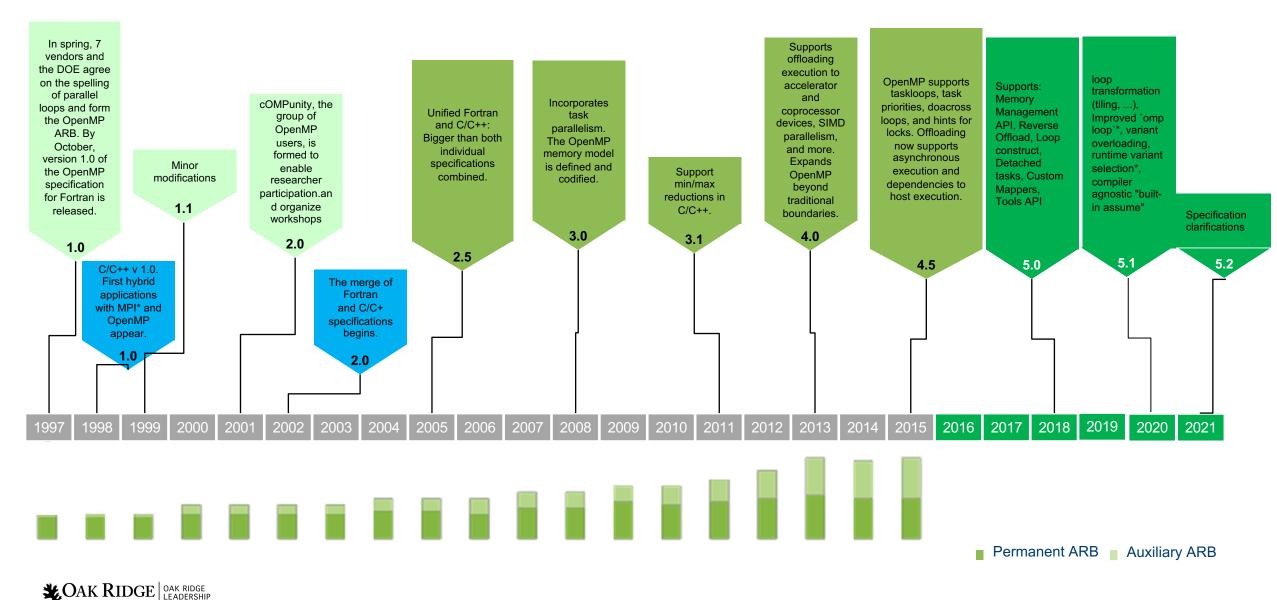
#### Introduction to OpenMP

It is a An Application Program Interface (API) to allow programmers to develop threaded parallel codes on shared memory computational units.

- Directives are understood by OpenMP aware compilers (others are free to ignore)
- Generates parallel threaded code
  - Original thread becomes thread "0"
  - Share resources of the original thread (or rank)
  - Data-sharing attributes of variables can be specified based on usage patterns



# History of OpenMP: 1997 - 2021



# **Recap: OpenMP Worksharing**



- Creates a team of OpenMP threads that execute the structured-block that follows
- Number of threads property is generally specified by OMP\_NUM\_THREADS env variable or num\_threads clause (num\_threads has precedence)

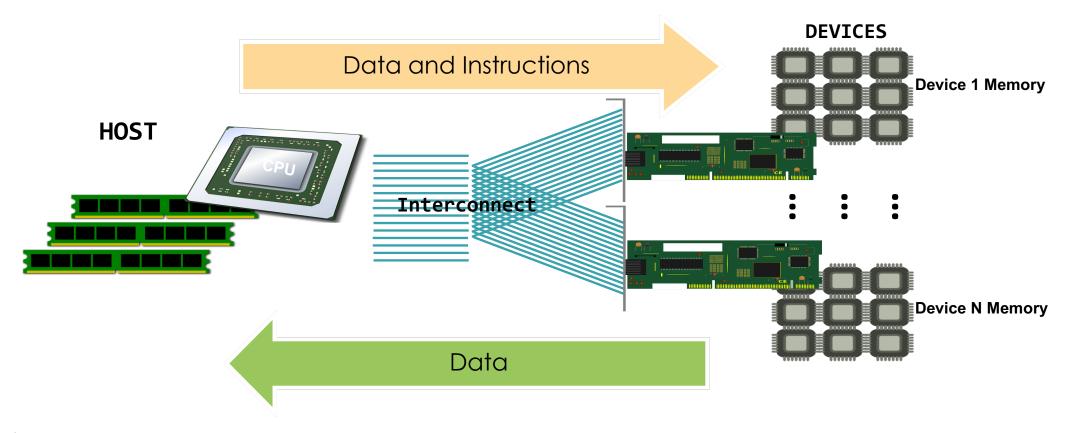
#### **Recap: OpenMP Worksharing**

Serial	Parallel	Parallel Worksharing
<pre>for (int i = 0; i &lt; N; ++i) {         C[i] = A[i] + B[i]; }</pre>	<pre>#pragma omp parallel for (int i = 0; i &lt; N; ++i) {         C[i] = A[i] + B[i]; }</pre>	<pre>#pragma omp parallel for for (int i = 0; i &lt; N; ++i) {         C[i] = A[i] + B[i]; }</pre>
<ul> <li>1 thread/process will execute each iteration sequentially</li> <li>Total time = time_for_single_iteration * N</li> </ul>	<ul> <li>Say, OMP_NUM_THREADS = 4</li> <li>4 threads will execute each iteration sequentially (overwriting values of C)</li> </ul>	<ul> <li>Say, OMP_NUM_THREADS = 4</li> <li>4 threads will distribute iteration space (roughly N/4 per thread)</li> <li>Total time =</li> </ul>

 Total time = time\_for\_single\_iteration \* N  Total time = time\_for\_single\_iteration \* N/4

#### Introduction: OpenMP Offload

• OpenMP offload constructs are a set of directives for C++ and Fortran that were introduced in OpenMP 4.0 and further enhanced in later versions.



Actional Laboratory

#### Summit vs. Frontier

512 GB (2x16x16 GB)

25 GB/s (2x12.5 GB/s)

83

X-Bus (SMP)

PCle Gen4

EDR IB

900 GB/s

900 GB/s

900 GB/s

HBM 16 GB

50 GB/s

HBM 16 GB

50 GB/s

HBM 16 GB

TF

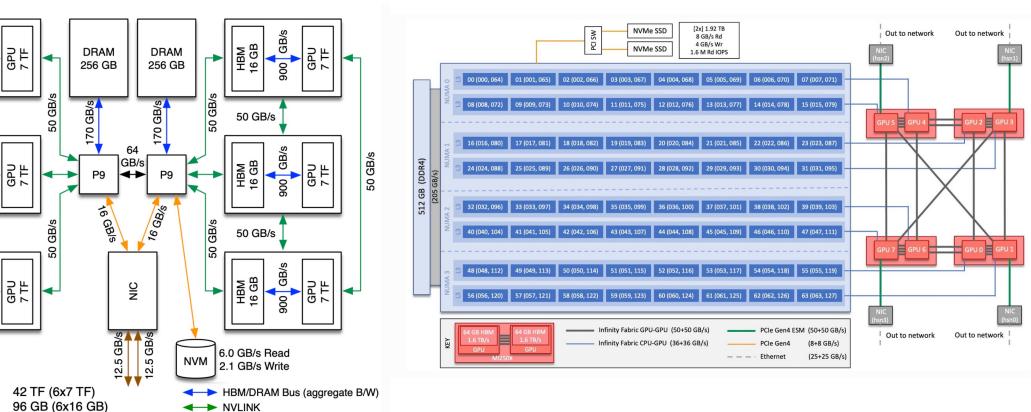
HBM

NET

DRAM

MMsq/s

50 GB/s



Summit Node

#### Frontier Node

#### **OpenMP Offload: Steps**

- Identification of compute kernels
  - CPU initiates kernel for execution on the device
- Expressing **parallelism** within the kernel

- Manage data transfer between CPU and Device
  - relevant data needs to be moved from host to device memory
  - kernel executes using device memory
  - relevant data needs to be moved from device to main memory

# **Step 1: Identification of Kernels to Offload**

- Look for compute intensive code and that can benefit from parallel execution
  - Use performance analysis tools to find bottlenecks
- Track independent work units with well defined data accesses
- Keep an eye on platform specs
  - GPU memory is a precious resource
- Confirm via Profiling
  - Tools like rocprof and HPCToolkit
  - More information regarding rocprof can be found at: <u>https://docs.olcf.ornl.gov/systems/frontier\_user\_guide.html#optimization-and-profiling</u>
  - More information on HPCToolkit can be found at: http://hpctoolkit.org

### How to Offload ?

C/C++	Fortran	Description
<b>#pragma omp target</b> [clause[ [,] clause] ] new- line structured-block	<pre>!\$omp target [clause[ [,] clause] ] loosely/tightly-structured-block !\$omp end target</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator.

- A device data environment is created for the structured block
- The code region is mapped to the device and executed.

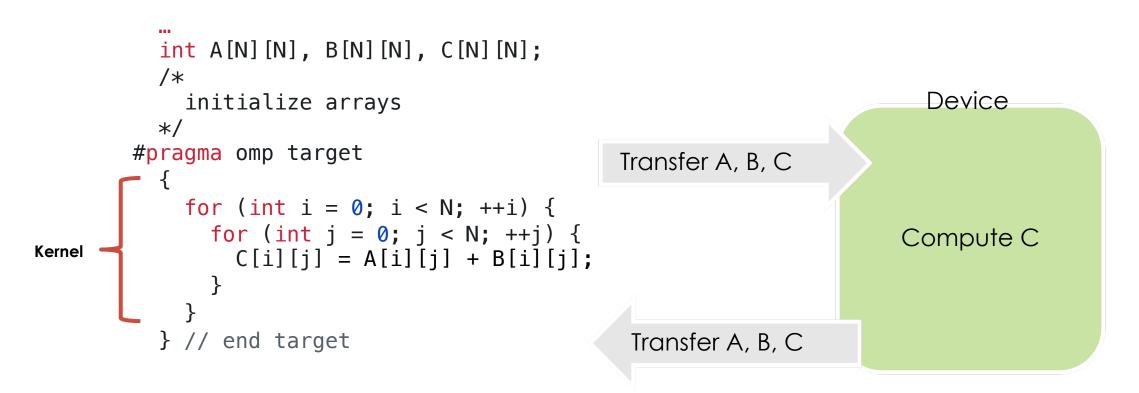


# **OpenMP Offload: Target Directive**

- Clauses allowed on the target directive:
  - if([ target :] scalar-expression)
  - device([ device-modifier :] integer-expression)
  - thread\_limit(integer-expression)
  - private(list)
  - firstprivate(list)
  - in\_reduction(reduction-identifier : list)
  - map([[map-type-modifier[,] [map-type-modifier[,] ...]] map-type: ] locator-list)
  - is\_device\_ptr(list)
  - has\_device\_addr(list)
  - defaultmap(implicit-behavior[:variable-category])
  - nowait
  - depend([depend-modifier,] dependence-type : locator-list)
  - allocate([allocator :] list)
  - uses\_allocators(allocator[(allocator-traits-array)] [,allocator[(allocator-traits-array)] ...])

# **OpenMP Offload: Example using omp target**

/\*C code to offload Matrix Addition Code to Device\*/



The target construct is a task generating construct

#### **Step 2: Expressing Parallelism**

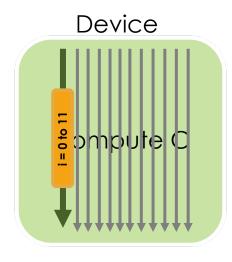
/\*C code to offload Matrix Addition Code to Device\*/

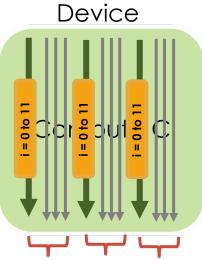
```
int A[N][N], B[N][N], C[N][N];
  /*
                                                                      Device
    initialize arrays
  */
#pragma omp target
                                          Transfer A, B, C
  Ł
    for (int i = 0; i < N; ++i) {</pre>
      for (int j = 0; j < N; ++j) {</pre>
                                                                     ombute (
                                                                                 Idle threads
        C[i][j] = A[i][j] + B[i][j];
       }
                                           Transfer A, B, C
  } // end target
```



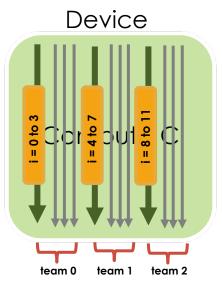
#### **Expressing Parallelism: Increasing device utilization**

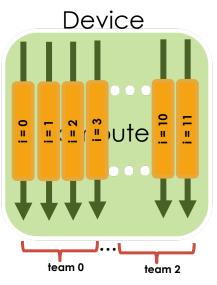
target	target teams	target teams distribute	target teams distribute parallel
<pre>#pragma omp target for (int i = 0; i &lt; 12; ++i) {         C[i] = A[i] + B[i]; }</pre>	<pre>#pragma omp target teams num_teams(3) for (int i = 0; i &lt; 12; ++i) {         C[i] = A[i] + B[i]; }</pre>	<pre>#pragma omp target teams distribute num_teams(3) for (int i = 0; i &lt; 12; ++i) {         C[i] = A[i] + B[i]; }</pre>	<pre>#pragma omp target teams distribute parallel for num_teams(3) for (int i = 0; i &lt; 12; ++i) {         C[i] = A[i] + B[i]; }</pre>





team 0 team 1 team 2





15 **CAK RIDGE** OAK RIDGE LEADERSHIP COMPUTING FACILITY

#### **Expressing Parallelism: Device Execution Directives**

C/C++	Fortran	Description
<b>#pragma omp target</b> [clause[ [,] clause] ] new-line structured-block	<pre>!\$omp target [clause[ [,] clause] ] loosely/tightly-structured-block !\$omp end target</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator.
<b>#pragma omp target teams</b> [clause[ [,] clause] ] new-line structured-block	<pre>!\$omp target teams [clause[ [,] clause] ] loosely/tightly-structured-block !\$omp end target teams</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. The <b>teams</b> construct creates a league of teams. The <b>initial</b> thread of each team executes the code region.
<b>#pragma omp target teams</b> <b>distribute</b> [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target teams distribute [clause[ [,] clause] ] loop-nest [!\$omp end target teams distribute]</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. A <b>league</b> of thread teams is created, and loop iterations are <b>distributed</b> and executed by the initial teams.
<b>#pragma omp target teams</b> <b>distribute parallel for</b> [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target teams distribute parallel do [clause[ [,] clause] ] loop-nest [!\$omp end target teams distribute parallel do] *need simd to map to threads</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. A <b>league</b> of thread teams are created, and loop iterations are <b>distributed</b> and <b>executed in parallel</b> by all threads of the teams.

# **Expressing Parallelism: Other combinations**

C/C++	Fortran	Description
<b>#pragma omp target parallel</b> [clause[ [,] clause] ] new-line structured-block	<pre>!\$omp target parallel [clause[ [,] clause] ] loosely-structured-block !\$omp end target parallel</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. The <b>parallel</b> construct creates a team of OpenMP threads that execute the region.
<b>#pragma omp target parallel for</b> [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target parallel do [clause[ [,] clause] ] loop-nest [!\$omp end target parallel do]</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. The <b>parallel for/do</b> combined construct creates a thread team and distributes the inner loop iterations over threads.
<b>#pragma omp target parallel</b> <b>loop</b> [clause[ [,] clause] ] new- line loop-nest	<pre>!\$omp target parallel loop [clause[ [,] clause] ] loop-nest [!\$omp end target parallel loop]</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. The <b>parallel</b> construct creates a team of OpenMP threads that execute the region. The <b>loop</b> construct allows concurrent execution of the associated loops.
<b>#pragma omp target teams loop</b> [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target teams loop [clause[ [,] clause] ] loop-nest [!\$omp end target teams loop]</pre>	The <b>target</b> construct offloads the enclosed code to the accelerator. The <b>teams</b> construct creates a league of teams. The <b>loop</b> construct allows concurrent execution of the associated loops.

#### **Expressing Parallelism : SIMD**

\* We will revisit this when we discuss Frontier specifics \*

C/C++	Fortran	Description
<b>#pragma omp target simd</b> [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target simd [clause[ [,] clause] ] loop-nest [!\$omp end target simd]</pre>	Semantics are identical to explicitly specifying a target directive immediately followed by SIMD directive.
<b>#pragma omp target parallel for simd</b> \ clause[[,] clause] ] new-line loop-nest	<pre>!\$omp target parallel do simd [clause[ [,] clause] ] loop-nest [!\$omp end target parallel do simd]</pre>	Semantics are identical to explicitly specifying a target directive immediately followed by a parallel worksharing-loop SIMD directive.
<b>#pragma omp target teams distribute simd</b> \ [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target teams distribute simd [clause[ [,] clause] ] loop-nest  [!\$omp end target teams distribute simd]</pre>	Semantics are identical to explicitly specifying a target directive immediately followed by a teams distribute simd directive
<b>#pragma omp target teams distribute parallel for simd \</b> [clause[ [,] clause] ] new-line loop-nest	<pre>!\$omp target teams distribute parallel do simd [clause[ [,] clause] ] loop-nest [!\$omp end target teams distribute parallel do simd]</pre>	Semantics are identical to explicitly specifying a target directive immediately followed by a teams distribute parallel worksharing-loop SIMD directive.

# **Useful RT Routines: Device Environment**

			Where to call			
	C/C++	Fortran	Host	Target region	Description	
	int omp_get_num_procs(void);	integer function omp_get_num_procs()	$\checkmark$	$\checkmark$	returns the number of processors available to the device	
	void omp_set_default_device(int device_num);	subroutine omp_set_default_device(device_num) integer device_num	$\checkmark$	X	sets the value of the default-device-var ICV of the current task to device_num	
	int omp_get_default_device(void);	integer function omp_get_default_device()	$\checkmark$	X	returns the default target device	
$\checkmark$	int omp_get_num_devices(void);	integer function omp_get_num_devices()	$\checkmark$	X	returns the number of non-host devices available for offloading code or data.	
-	nt omp_get_device_num(void);	integer function omp_get_device_num()	$\checkmark$	$\checkmark$	returns the device number of the device on which the calling thread is executing	
-	int omp_is_initial_device(void);	logical function omp_is_initial_device()	$\checkmark$	$\checkmark$	returns true if the current task is executing on the host otherwise, it returns false.	
	int omp_get_initial_device(void);	integer function omp_get_initial_device()	$\checkmark$	X	return the device number of the host device	

# **Teams Region: Useful RT Routines**

	Fortran	Where to call ?		
C/C++		Host	Target region	Description
int omp_get_num_teams(void);	integer function omp_get_num_teams()	$\checkmark$	$\checkmark$	returns the number of initial teams in the current teams region.
int omp_get_team_num(void);	integer function omp_get_team_num()	$\checkmark$	$\checkmark$	returns the initial team number of the calling thread
void omp_set_num_teams(int num_teams);	subroutine omp_set_num_teams(num_teams) integer num_teams	$\checkmark$	$\checkmark$	the number of threads to be used for subsequent teams regions that do not specify a num_teams clause
int omp_get_max_teams(void);	integer function omp_get_max_teams()	$\checkmark$	$\checkmark$	returns an upper bound on the number of teams that could be created by a teams construct
<pre>void omp_set_teams_thread_limit(i nt thread_limit);</pre>	subroutine omp_set_teams_thread_limit(thread_li mit) integer thread_limit	~	~	defines the maximum number of OpenMP threads per team

#### References

- Examples were adapted from: <u>https://github.com/SOLLVE/sollve\_vv</u>
- OpenMP Specification (5.x)
  - https://www.openmp.org/specifications/
- <a href="https://www.nas.nasa.gov/hecc/assets/pdf/training/OpenMP4.5\_3-20-19.pdf">https://www.nas.nasa.gov/hecc/assets/pdf/training/OpenMP4.5\_3-20-19.pdf</a>
- OpenMP Disussion @ 2021 Exascale Computing Project Virtual Annual Meeting (April 12 16, 2021)