Code Development: Challenges in developing the chemistry codes of tomorrow

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Motivation

- \blacktriangleright Conventional CCSD(T) calculations scale as N^7
- \blacktriangleright Memory requirements scale as N^4
- \blacktriangleright Heavy parallelism on modern super computers (Apra et $al.$ ¹, 20 Water molecules)

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Motivation

 \blacktriangleright Conventional CCSD(T) calculations scale as N^7

$$
2h \xrightarrow{\times 2} 256h
$$

 \blacktriangleright Memory requirements scale as N^4

$$
460GB \xrightarrow{\times 2} 7TB
$$

- \blacktriangleright Heavy parallelism on modern super computers (Apra et $al.$ ¹, 20 Water molecules)
- \blacktriangleright ... does not solve the fundamental problem

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 $1E$. Apra et al. "Liquid Water: Obtaining the right answer for the right reasons". In: SC09, submission for Gordon Bell prize (2009).

Motivation

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- \blacktriangleright Heavy parallelism on modern super computers (Apra et $al.$ ¹, 20 Water molecules)
- \blacktriangleright ... does not solve the fundamental problem \rightarrow DEC
	- \blacktriangleright fragmentize problem
	- \blacktriangleright independent fragment calculations
		- \Rightarrow Distribution of memory and work

 $1E$. Apra et al. "Liquid Water: Obtaining the right answer for the right reasons". In: SC09, submission for Gordon Bell prize (2009).

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 \triangleright DEC constitutes a linear-scaling and massively parallel framework **Advantage and Problem:** Rigorous error control

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Motivation II

- \triangleright DEC constitutes a linear-scaling and massively parallel framework **Advantage and Problem:** Rigorous error control
- \blacktriangleright Fragment size a priori unknown
- Size grows with requested precision
- \triangleright Computational complexity scales as traditional methods with $N_{\rm frag}^7$
	- \Rightarrow Parallelization of fragment calculations necessary

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Motivation: CCSD(T)

N 6 frag

 $N_{\rm frag}^5$

N 7 frag

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Compiler choice

- \blacktriangleright Test of Intel vs. Cray
- \triangleright Cray preferred because it is OpenACC enabled

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[Compiler choice](#page-8-0)

Compiler choice

- \blacktriangleright Test of Intel vs. Cray
- \triangleright Cray preferred because it is OpenACC enabled
- \triangleright For the investigated systems, speedup of 5-8x

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Coarse-grained parallelism

- \triangleright DEC has 3 levels of parallelism
- \triangleright Coarse granularity is given by DEC-splitting

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Coarse-grained parallelism: Scaling

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Coarse-grained parallelism: Scaling

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- \blacktriangleright Heavy work $N_{\rm frag}^7$
- \blacktriangleright Heavy memory requirements $\mathcal{N}^4_{\text{frag}}$

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- \blacktriangleright Heavy work $N_{\rm frag}^7$
- \blacktriangleright Heavy memory requirements $\mathcal{N}^4_{\text{frag}}$

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Fine-grained Parallelism

- \blacktriangleright High memory requirements per fragment \Rightarrow use all memory per node (1ppn)
- ightharpoonup test OMP with aprun $-cc$ 0, 2, 4, ..., 1, 3, ...

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1 Picture from

https://www.olcf.ornl.gov/wp-content/uploads/2012/11/opteron 6274 cpu.png

Fine-grained Parallelism

- \blacktriangleright High memory requirements per fragment \Rightarrow use all memory per node (1ppn)
- Slave 1 Slave 2 Slave 3 Initialization Batched integral loop Striped matrix multiplications Low order contractions and finalization MPI_BCAST MPI_Allreduce MPI_REDUCE MPI_PUT MPI_GET MPI_PUT MPL LINE MPI_ACCUMULATE fine-grained (OpenMP/OpenACC)
- ightharpoonup test OMP with aprun $-cc$ 0, 2, 4, ..., 1, 3, ...

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Fine-grained Parallelism

Figure : Timing of the algorithm with respect to the threads

aprun $-n$ \$MPIPROC $-N$ 1 $-d$ 8 $-j$ 1

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Fine-grained parallelism: OpenMP and OpenACC

- \triangleright OpenMP is used explicitly for the calculation of integrals
- \triangleright OpenMP is otherwise provided by threaded libraries

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Fine-grained parallelism: OpenMP and OpenACC

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- OpenACC is used in the (T) correction
- OpenACC implementation of the integral calculation is in progress
- \triangleright OpenACC implementations for MP2 and CCSD residual are considered

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Use of directives for portability, ease of implementation and code maintenance

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Fine-grained parallelism: Accelerators in (T)

Fine-grained parallelism: Accelerators in (T)

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- \blacktriangleright unstructured data regions
- 10 unique async handles
- asynchronous wait directives to avoid race conditions
- \triangleright calls to dgemm acc openacc async of the cray-libsci acc library

Diverse parallelism. Specific for the level of theory. Difficult to implement, but traditional parallelization techniques can be used

- \blacktriangleright Non-linear equation solver
- \blacktriangleright Residual routines
- \blacktriangleright perturbative corrections

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$$
\begin{array}{l} \text{(tasks/memory: } N_{\text{frag}}^4 / N_{\text{frag}}^4\text{)}\\ \text{(tasks/memory: } N_{\text{frag}}^6 / N_{\text{frag}}^4\text{)}\\ \text{(tasks/memory: } N_{\text{frag}}^7 / N_{\text{frag}}^4\text{)}\end{array}
$$

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Diverse parallelism. Specific for the level of theory. Difficult to implement, but *traditional* parallelization techniques can be used

 \blacktriangleright Non-linear equation solver (tasks/memory: N 4 frag*/*N 4 frag)

(tasks/memory: N 6 frag*/*N 4 frag)

- Residual routines
- \blacktriangleright perturbative corrections (tasks/memory: N 7 frag*/*N 4 frag)

 \Rightarrow distribution of tasks $(N_{\rm frag}^7)$ and memory $(N_{\rm frag}^4)$ using one-sided MPI routines in a tiled tensor framework.

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 \triangleright Use of the conjugate-residual technique with preconditioning

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- \triangleright Distribute tensors in PDM

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- \triangleright Use of the conjugate-residual technique with preconditioning
- \triangleright Requires to save iterative subspace information for fast and stable convergence \rightarrow employ CROP solver, only use last 4 iterations
- \triangleright Distribute tensors in PDM
- Only vector-vector operations, easy to perform in PDM

CCSD

 \blacktriangleright Construction of r_n scales as N_{frag}^6

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- \blacktriangleright Construction of r_n scales as N_{frag}^6
- \triangleright Avoid big intermediates by batching and remote updates (MPI ACCUMULATE)

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- \blacktriangleright Construction of r_n scales as N_{frag}^6
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- \blacktriangleright Exploit symmetry

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- \triangleright Avoid big intermediates by batching and remote updates (MPI ACCUMULATE)
- Avoid storing \rightarrow recalculate
- \blacktriangleright Exploit symmetry
- \triangleright Several CCSD schemes with different focus on N_{frac}

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Medium-grained parallelism: Solving the CCSD problem

- \blacktriangleright Time in solver is negligible
- \triangleright Use best scheme for current fragment
- \triangleright Use DEC parallelism to ensure most efficient distribution of fragment

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 \triangleright Corresponding to solving an equation in one step

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- \triangleright Corresponding to solving an equation in one step
- \triangleright Two methods are currently implemented: MP2, (T)

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- \triangleright Corresponding to solving an equation in one step
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- \triangleright Characterized by calculation of integrals and matrix-matrix multiplications

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- \triangleright Corresponding to solving an equation in one step
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- \triangleright Characterized by calculation of integrals and matrix-matrix multiplications
- Scaling MP2: N_{frag}^5 and (T): N_{frag}^7

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- \blacktriangleright "Easy" and efficient to port to GPUs \rightarrow 5-9x speedup for (T)

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- Scaling MP2: N_{frag}^5 and (T): N_{frag}^7
- \blacktriangleright "Easy" and efficient to port to GPUs \rightarrow 5-9x speedup for (T)
- \triangleright Simple parallelization over many nodes by batching

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Medium-grained parallelism: Solving the (T) problem

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CCSD INTs (1)

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 \triangleright One sided MPI calls are handy, communication can be hidden and save memory

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 \triangleright One sided MPI calls are handy, communication can be hidden and save memory

BUT:

 \triangleright Used asynchronously terrible, especially MPI $ACCUMULATE$ \rightarrow partial serialization

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CPU+GPU vs CPU timings for Integrals

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SOLUTION:

- \triangleright Asynchronous progress engine (communication threads in MPI)
- \triangleright Efficient use with CLE5.2 and MPI3 possible

```
aprun -e MPICH_NEMESIS_ASYNC_PROGRESS=1
-e MPICH MAX THREAD SAFETY=multiple
-e MPICH RMA OVER DMAPP=1
-n $MPIPROC -N 1 -d 8
-cc 0.2.4...1.3... -r 1
```
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 \blacktriangleright Memory, more per node and per core

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- \blacktriangleright Memory, more per node and per core
- \blacktriangleright More memory on the accelerators

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- \blacktriangleright Memory, more per node and per core
- \blacktriangleright More memory on the accelerators
- Either OpenACC or OpenMP-4 compatible use of accelerators

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- \triangleright General purpose codes should work, i.e. all standards should be supported
- \triangleright More support for asynchronicity (MPI/GPU)
- Large molecular systems are in the focus of science \rightarrow more nodes or more runtime

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