Performance Measurement and Analysis Tools for the Cray XK System

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Strengths

Provide a complete solution from instrumentation to measurement to analysis to visualization of data

Performance measurement and analysis on large systems

- Automatic Profiling Analysis
- Load Imbalance
- HW counter derived metrics
- Predefined trace groups provide performance statistics for libraries called by program (blas, lapack, pgas runtime, netcdf, hdf5, etc.)
- Observations of inefficient performance
- Data collection and presentation filtering
- Data correlates to user source (line number info, etc.)
- Support MPI, SHMEM, OpenMP, UPC, CAF, OpenACC
- Access to network counters
- Minimal program perturbation

The Cray Performance Analysis Framework

Supports traditional post-mortem performance analysis

- Automatic identification of performance problems
 - Indication of causes of problems
 - Suggestions of modifications for performance improvement
- pat_build: provides automatic instrumentation
- CrayPat run-time library collects measurements (transparent to the user)
- pat_report performs analysis and generates text reports
- pat_help: online help utility
- Cray Apprentice2: graphical visualization tool

• To access software:

module load perftools

Where to Run Instrumented Application

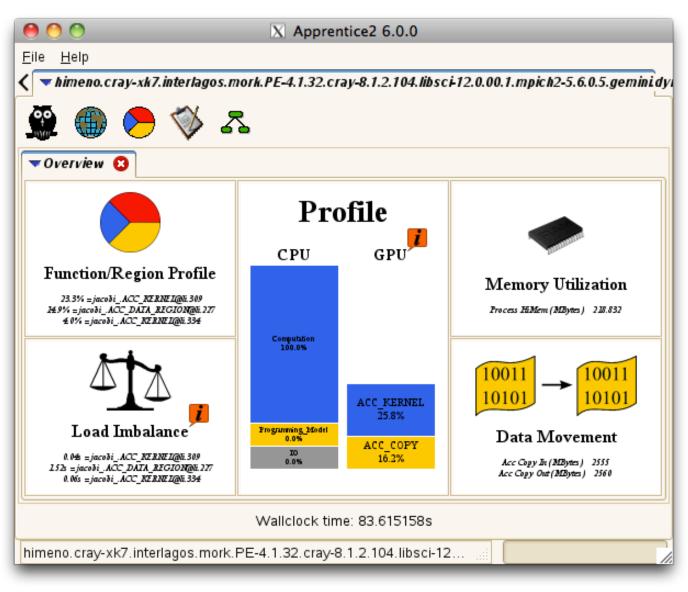
- By default, data files are written to the execution directory
- Default behavior requires file system that supports record locking, such as Lustre (/mnt/snx3/..., /lus/..., /scratch/ ...,etc.)
 - Can use PAT_RT_EXPFILE_DIR to point to existing directory that resides on a high-performance file system if not execution directory

Number of files used to store raw data

- 1 file created for program with 1 256 processes
- \sqrt{n} files created for program with 257 *n* processes
- Ability to customize with PAT_RT_EXPFILE_MAX

See intro_craypat(1) man page

Apprentice2 Overview



Sampling with Line Number information

00		heid	i@limited: /h/heidi — ssł	- 81×26	
Table 2:	Profile by	Group, Fun	ction, and Line		
Samp% 	S 		& Function Source Line PE=HIDE		
100.0%	8376.9		Total		
	7804.0		IUSER		
 51.7% 3 	6 4328.7 		calc3_ heidi/DAR	PA/cache_util/calc3.do300-ijswap	F
4 15.			6.8% line.78 7.9% line.79		
			7.6% line.80 21.2% line.93		
4 1.	1% 88.4	22.6	20.8% line.94		ſ
	0% 84.5 0% 86.8		17.6% line.95 28.2% line.96		
4 1.	3% 105.0	23.0	18.4% line.97 17.7% line.98		
	-+/0 110.5	1 24.3	8		4
				144,1 389	6 💾

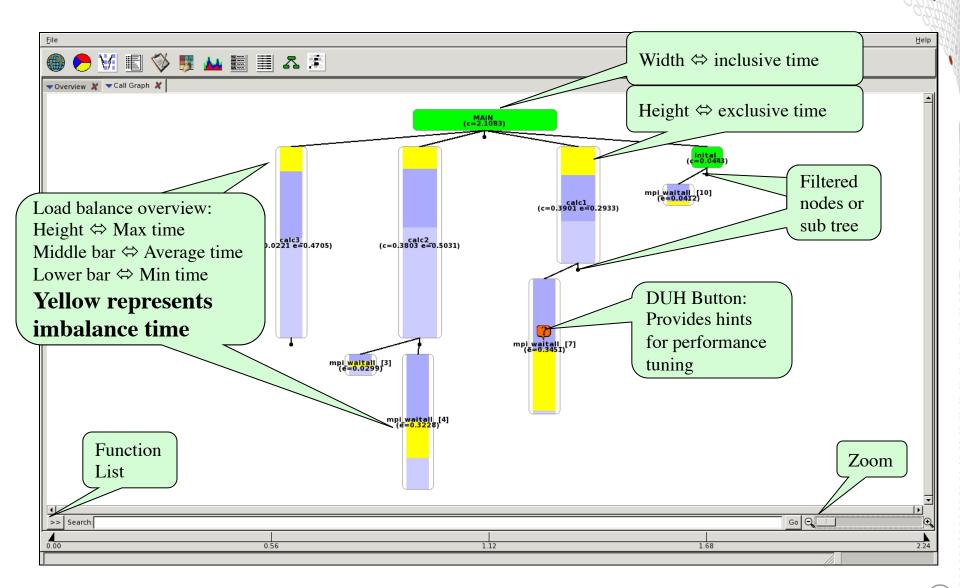
MPI Messages By Caller

00	heidi@limited: /h/heidi — ssh — 81×26	
Table 4: M	IPI Message Stats by Caller	
MPI Msg	g MPI MsgSz 4KB<= Function	
Bytes	s Msg <16B MsgSz Caller	
	Count Count <64KB PE=[mmm]	
	I I I Count I	
140166953.	8 8890.6 339.8 8550.8 Total	
140166833	3.8 8875.6 324.8 8550.8 MPI_ISEND	
	00.0 4850.0 75.0 4775.0 calc2_	
31	I I I I shalow_	
	0800.0 7200.0 2400.0 4800.0 pe.0	
	.600.0 4800.0 0.0 4800.0 pe.1	
	0800.0 4800.0 1200.0 3600.0 pe.47	
	00.0 3725.0 100.0 3625.0 calc1_	
3	shalow_	
1111		
	0800.0 7200.0 2400.0 4800.0 pe.0	
	200.0 3600.0 0.0 3600.0 pe.1	
	200.0 3600.0 0.0 3600.0 pe.24	
1 · · · · · · · · · · · · · · · · · · ·		
		624,3
1		

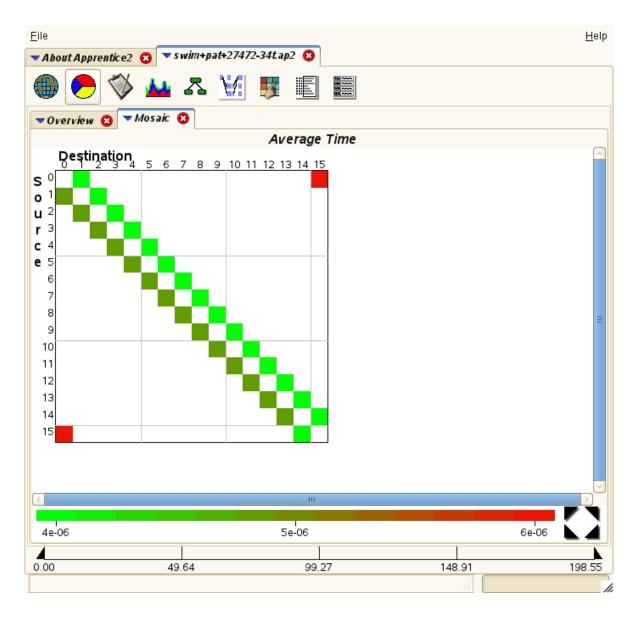
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79%

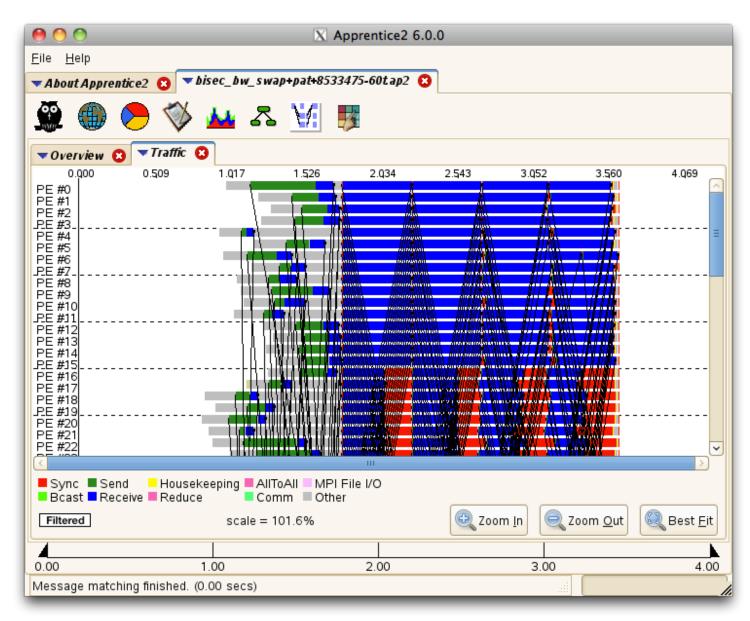
Call Tree View



Mosaic View – Shows Communication Pattern



Traffic Report – MPI Communication Timeline



Porting to a Hybrid or Many-core System

A Porting and Optimization Strategy for Hybrid and Many-core Systems

- Maximize on-node communication between MPI ranks
- Relieve on-node shared resource contention by pairing threads or processes that perform different work (for example computation with off-node communication) on the same node
- Add parallelism to MPI ranks to take advantage of cores within a node while minimizing network injection contention
- Accelerate work intensive parallel loops

Grid Detection and Rank Reordering Suggestions

Automatic Communication Grid Detection

- Analyze runtime performance data to identify grids in a program to maximize on-node communication
 - Example: nearest neighbor exchange in 2 dimensions
 - Sweep3d uses a 2-D grid for communication
- Determine whether or not a custom MPI rank order will produce a significant performance benefit
- Grid detection is helpful for programs with significant point-to-point communication
- Doesn't interfere with MPI collective communication optimizations

Automatic Communication Grid Detection (2)

- Tools produce a custom rank order if it's beneficial based on grid size, grid order and cost metric
- Heuristics available for:
 - MPI sent message statistics
 - User time (time spent in user functions) can be used for PGAS codes
 - Hybrid of sent message and user time)
- Summarized findings in report
- Available with sampling or tracing
- Describe how to re-run with custom rank order

MPI Rank Order Observations

Table 1: Profile by Function Group and Function Time% Time Imb. Imb. Calls Group Time Time% Function **PE=HIDE** 21621.0 |Total 100.0% 463.147240 52.0% 240.974379 21523.0 MPI 36.214468 47.7% 221.142266 14.1% 10740.0 mpi recv 19.829001 25.849906 MPI SEND 56.7% 10740.0 4.3% 200.474690 32.0 USER 43.3% 41.0% 189.897060 58.716197 23.6% 12.0 sweep 1.6% 7.579876 1.899097 20.1% 12.0 source 4.7% 21.698147 39.0 MPI SYNC 99.6% 4.3% 20.005424 mpi allreduce (sync) 20.091165 32.0 0.0% 0.000024 27.0 SYSCALL

MPI Rank Order Observations (2)

MPI Grid Detection:

There appears to be point-to-point MPI communication in a 96 X 8 grid pattern. The 52% of the total execution time spent in MPI functions might be reduced with a rank order that maximizes communication between ranks on the same node. The effect of several rank orders is estimated below.

A file named MPICH_RANK_ORDER.Grid was generated along with this report and contains usage instructions and the Custom rank order from the following table.

Rank Order	On-Node Bytes/PE	On-Node Bytes/PE% of Total Bytes/PE	MPICH_RANK_REORDER_METHOD
Custom	2.385e+09	95.55%	3
SMP	1.880e+09	75.30%	1
Fold	1.373e+06	0.06%	2
RoundRobin	0.000e+00	0.00%	0

MPICH_RANK_ORDER File

```
# The 'Custom' rank order in this file targets nodes with multi-core
# processors, based on Sent Msg Total Bytes collected for:
#
# Program: /lus/nid00030/heidi/sweep3d/mod/sweep3d.mpi
# Ap2 File: sweep3d.mpi+pat+27054-89t.ap2
# Number PEs: 48
# Max PEs/Node: 4
#
# To use this file, make a copy named MPICH_RANK_ORDER, and set the
# environment variable MPICH_RANK_REORDER_METHOD to 3 prior to
# executing the program.
#
# The following table lists rank order alternatives and the grid_order
# command-line options that can be used to generate a new order.
. . .
```

Auto-Generated MPI Rank Order File

-					
	<pre># The 'USER_Time_hybrid' rank order in this</pre>	,443,9,467,25,499,105,	1,415,13,471,45,503,29	3,440,35,432,67,400,99 ,408,11,464,43,496,27, 472,51,504	5,273,337,609,369,577,
	<pre>file targets nodes with multi-core # processors, based on</pre>		1,391,109,423,93,455,1	19,392,75,424,59,456,8 3,384,107,416,91,488,1 15,448,123,480	1,585,537,601,289,553,
	Sent Msg Total Bytes collected for: #	6,436,102,468,70,404,3 8,412,14,444,46,476,11	2,530,34,562,66,538,98 ,522,10,570,42,554,26,	132,401,196,441,164,40 9,228,433,236,465,204, 473,244,393,188,497	256,373,261,341,264,34 9,280,317,272,381,269,
	nid00023/malice/	86,396,30,428,62,460,5	18,514,74,586,58,626,8 2,546,106,634,90,578,1	252,505,140,425,212,45 7,156,385,172,417,180,	352,301,320,325,288,35 7,328,304,360,312,376,
	<pre>demo/Rank/sweep3d/src/ sweep3d # Ap2 File:</pre>	129,563,193,531,161,57 1,225,539,241,595,233,	135,315,167,339,199,34 7,259,307,231,371,239,	131,534,195,542,163,56 6,227,526,235,574,203, 598,243,558,187,606	258,338,266,346,282,31 4,274,370,766,306,710,
	sweep3d.gmpi-u.ap2 # Number PEs: 768	153,587,169,627,137,63 5,201,619,177,515,145,	175,363,159,323,143,35 5,255,291,207,275,183,	251,590,211,630,179,63 8,139,622,155,550,171, 518,219,582,147,614	646,298,750,322,718,35 4,758,290,734,662,686,
	<pre># Max PEs/Node: 16 # # To use this file,</pre>	7,405,71,469,39,437,10 3,413,47,445,15,509,79	133,406,197,438,165,47 0,229,414,245,446,141,	761,660,737,652,705,66 8,745,692,673,700,641, 684,713,644,753,724	262,375,263,343,270,31 1,271,351,286,319,278,
	make a copy named MPICH_RANK_ORDER, and set the	111,397,63,461,55,429, 87,421,23,493,119,389,	157,510,189,462,173,43 0,205,390,149,422,213,	729,732,681,756,721,71 6,764,676,697,748,689, 657,740,665,649,708	294,318,358,383,359,31 0,295,382,326,303,327,
	HOD to 3 prior to	134,402,198,434,166,41 0,230,442,238,466,174,	130,316,260,340,194,37 2,162,348,226,308,234,		765,661,709,663,741,65 3,711,669,767,655,743,
	<pre># executing the program. #</pre>	190,498,254,426,142,45 8,150,386,182,418,206,	202,364,186,324,154,35 6,138,292,170,276,178,	728,584,680,624,720,51 2,696,632,688,616,664, 544,608,656,648,576	677,727,751,693,647,70 1,717,687,757,685,733,
	0,532,64,564,32,572,96 ,540,8,596,72,524,40,6 04,24,588	128,533,192,541,160,56 5,232,525,224,573,240,	4,535,36,543,68,567,10	762,659,738,651,706,66 7,746,643,714,691,674,	
	104,556,16,628,80,636, 56,620,48,516,112,580, 88,548,120,612		52,591,20,631,60,639,8 4,519,108,623,92,551,1	722,731,763,658,642,75 5,739,675,707,650,682,	

Approach to Adding Parallelism

1. Identify possible accelerator kernels

- Determine where to add additional levels of parallelism
 - Assumes MPI application is functioning correctly on X86
 - Find top serial work-intensive loops (perftools + CCE loop work estimates)

2. Perform parallel analysis, scoping and vectorization

- Split loop work among threads
 - Do parallel analysis and restructuring on targeted high level loops
 - Use CCE loopmark feedback, Reveal loopmark and source browsing

3. Move to OpenMP and then to OpenACC

- Add parallel directives and acceleration extensions
 - Insert OpenMP directives (Reveal scoping assistance)
 - Run on X86 to verify application and check for performance improvements
 - Convert desired OpenMP directives to OpenACC

4. Analyze performance from optimizations

Step 1 - Identify possible accelerator kernels

• Helps identify high-level serial loops to parallelize

- Based on runtime analysis, approximates how much work exists within a loop
- Provides min, max and average trip counts that can be used to approximate work and help carve up loop on GPU

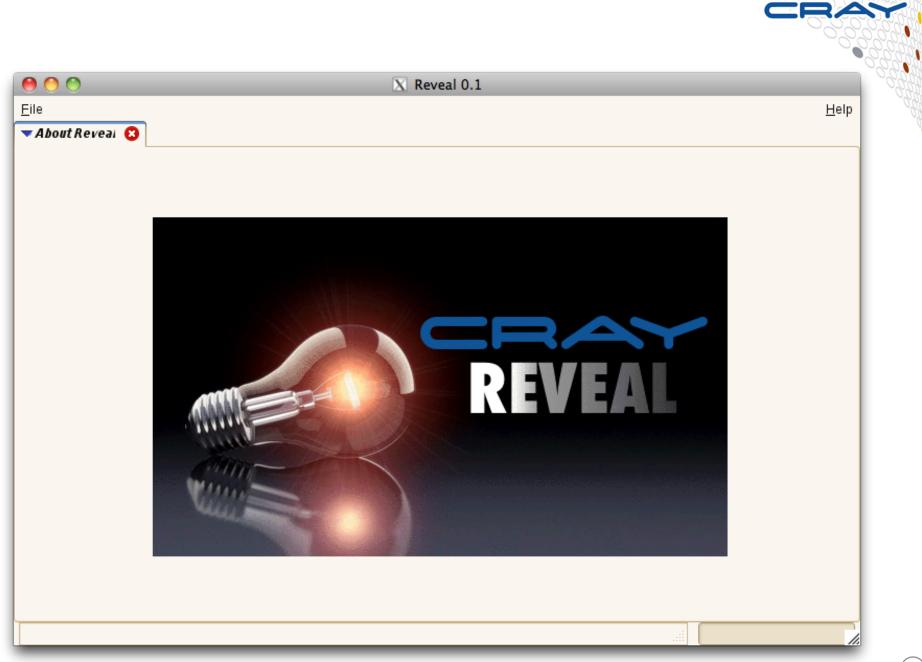
Collecting Loop Statistics

- Load PrgEnv-cray module
- Load perftools module
- Compile AND link with –h profile_generate
- Instrument binary for tracing
 - pat_build –w my_program
- Run application
- Create report with loop statistics
 - pat_report my_program.xf > loops_report

Example Report – Inclusive Loop Time

Table 2: L	oop Stats by	y Function	(from -	hprofile_g	generate)
Loop	Loop	Loop	Loop	Loop	Function=/.LOOP[.]
Incl	Hit	Trips	Trips	Trips	PE=HIDE
Time		Avg	Min	Max	
Total	l l	l l			
8.995914	100	25	0	25	sweepyLOOP.1.li.33
8.995604	2500	25	0	25	sweepyLOOP.2.li.34
8.894750	50	25	0	25	sweepzLOOP.05.li.49
8.894637	1250	25	0	25	sweepzLOOP.06.li.50
4.420629	50	25	0	25	sweepx2LOOP.1.li.29
4.420536	1250	25	0	25	sweepx2LOOP.2.li.30
4.387534	50	25	0	25	sweepx1LOOP.1.li.29
4.387457	1250	25	0	25	sweepx1LOOP.2.li.30
2.523214	187500	107	0	107	riemannLOOP.2.li.63
1.541299	20062500	12	0	12	riemannLOOP.3.li.64
0.863656	1687500	104	0	108	parabolaLOOP.6.li.67

Step 2 - Perform parallel analysis, scoping and vectorization R **Step 3 - Move to OpenMP and then to OpenACC**



Reveal

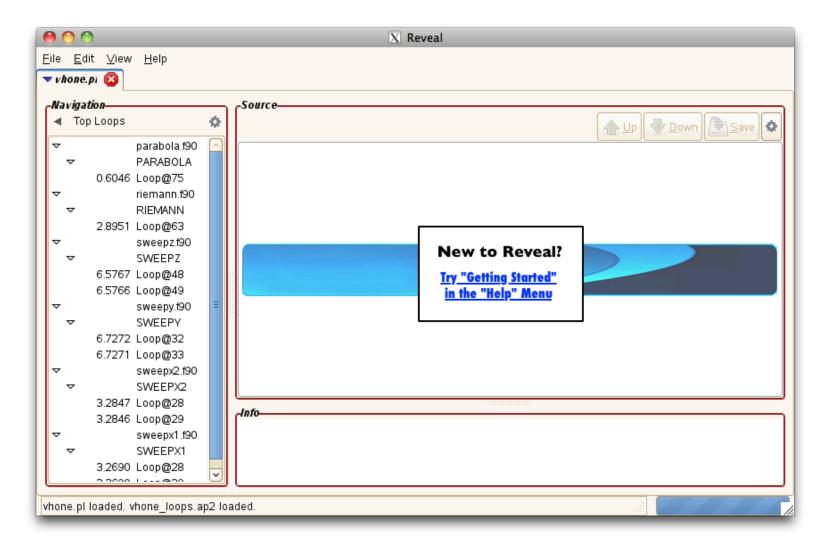
New code analysis and restructuring assistant...

 Uses both the performance toolset and CCE's program library functionality to provide static and runtime analysis information

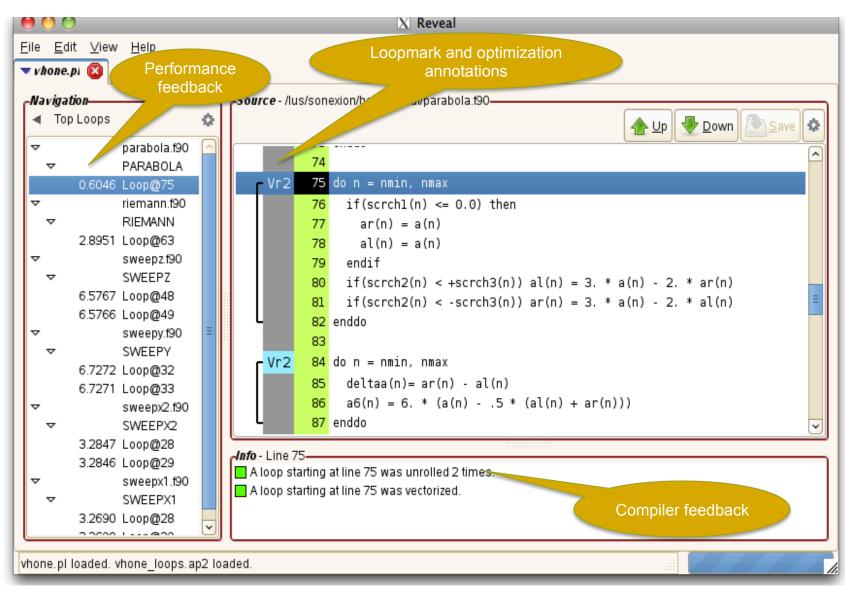
Key Features

- Annotated source code with compiler optimization information
 - Feedback on critical dependencies that prevent optimizations
- Scoping analysis
 - Identify, shared, private and ambiguous arrays
 - Allow user to privatize ambiguous arrays
 - Allow user to override dependency analysis
- Source code navigation based on performance data collected through CrayPat

Reveal with Loop Work Estimates

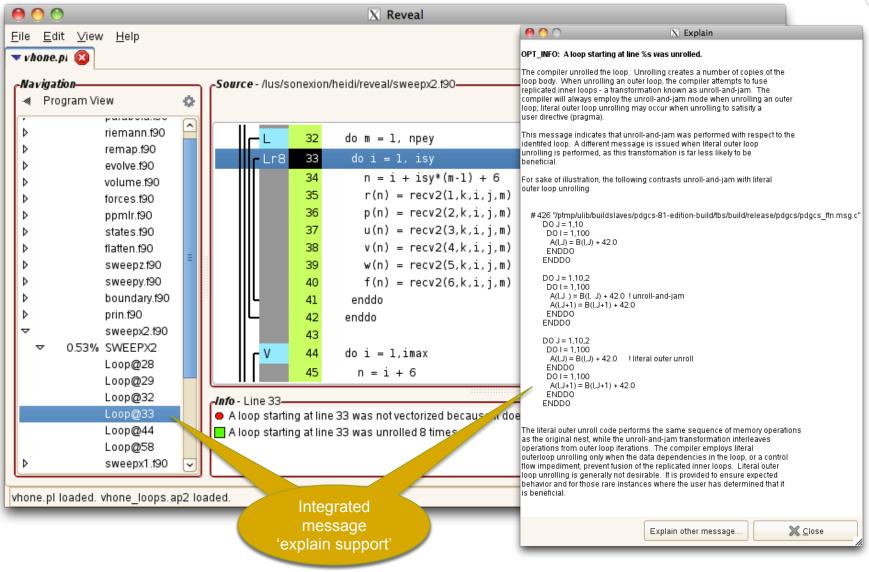


Visualize Loopmark with Performance Information



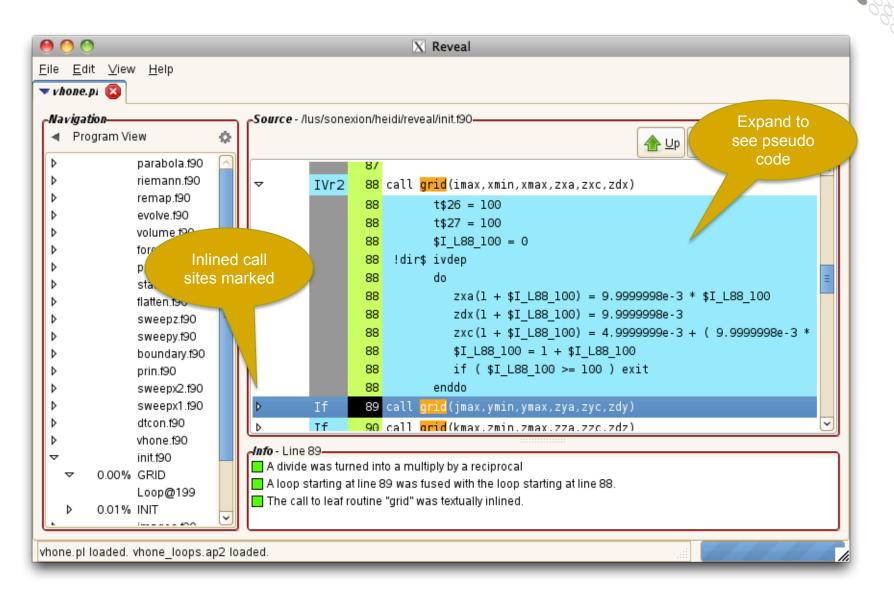
OLCF Workshop, January 2013

Visualize CCE's Loopmark with Performance Profile (2)

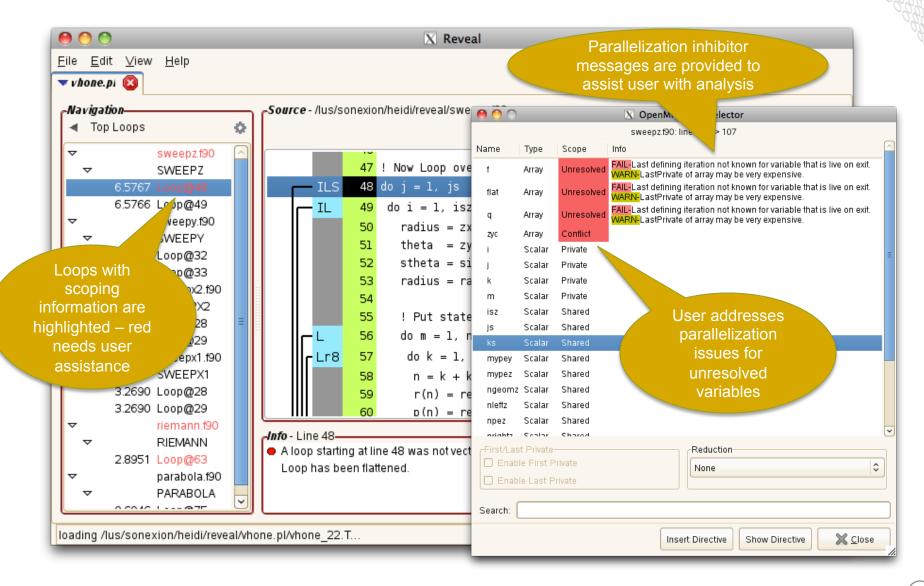


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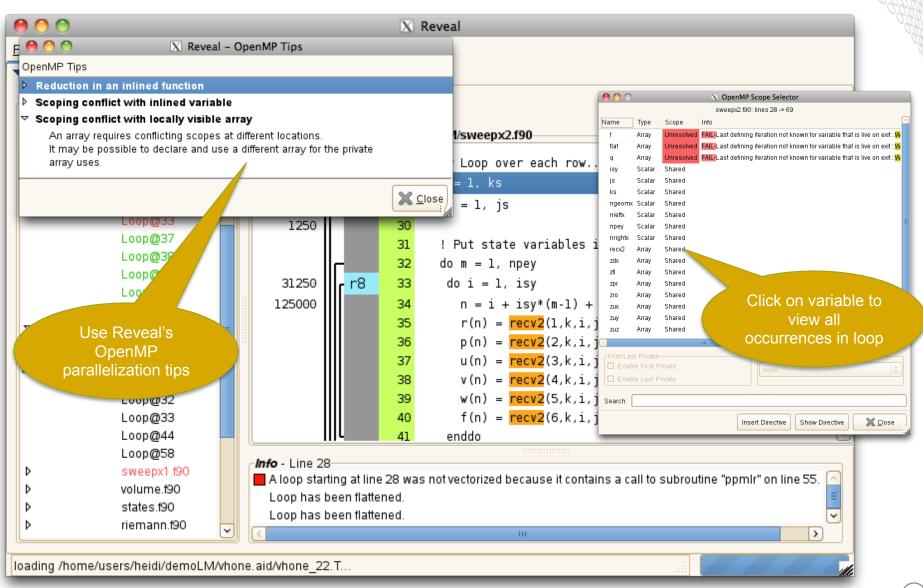
View Pseudo Code for Inlined Functions



Scoping Assistance – Review Scoping Results

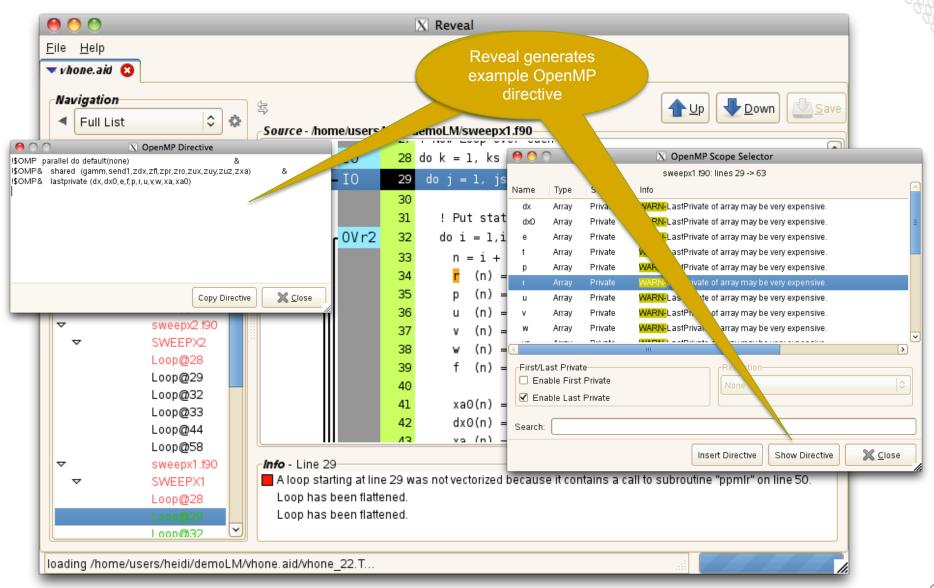


Scoping Assistance – User Resolves Issues



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Scoping Assistance – Generate Directive



Step 4 – Analyze performance of accelerator regions

Programming Models Supported for the GPU

- Goal is to provide whole program analysis for programs written for x86 or hybrid x86 + GPUs
- Development focus is on support of CCE with OpenACC directives

Cray XK programming models supported
 OpenACC, CUDA, PGI acc (or OpenACC) directives

Collecting GPU Statistics for OpenACC

- Load PrgEnv-cray module
- Load perftools module

Instrument binary for tracing and collecting GPU statistics

- pat_build –u –g mpi,blas my_program
- Run application

• Create report with GPU statistics

• pat_report my_program.xf > GPU_stats_report

Analyze Performance of Accelerated Program

Statistics collected for programs with OpenACC directives

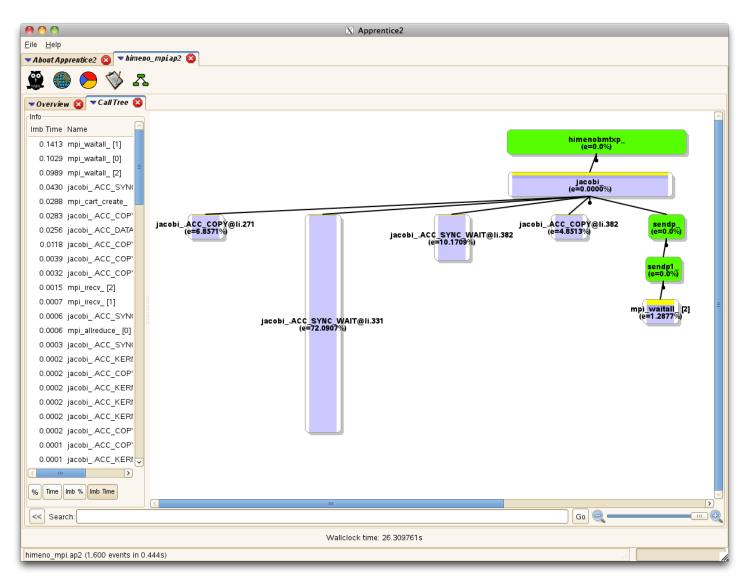
- Number of GPUs used in the job
- Host time for kernel launches, data copies and synchronization with the accelerator
- Accelerator time for kernel execution and data copies
- Data copy size to and from the accelerator
- Kernel grid size
- Block size
- Amount of shared memory dynamically allocated for kernel
- GPU performance counters
- Derived metrics based on performance counters

Profile with GPU

O O X Apprentice2										
<u>E</u> ile <u>H</u> elp										
🕶 About Apprentice2 🔯 🔻 himeno_mpi ap2 🚳										
🚇 🛞 🗢 🗇 љ										
▼Overview 😢 ▼CallTree 😢 ▼Text 😢										
(For percentages relative to next level up, specify: -s percent=r[elative])										
Table 1: Profile by Function Group and Function										
Time% Time Imb. Imb. Calls Group Time Time% Function PE=HIDE										
100.0% 25.675919 55473.0 Total										
72.1% 18.521376 0.042997 0.3% 1003.0 jacobiACC_SYNC_WAIT@li.331 10.2% 2.612815 0.000594 0.0% 1003.0 jacobiACC_SYNC_WAIT@li.382 6.9% 1.761435 0.011827 0.8% 1003.0 jacobiACC_COPY@li.271 4.9% 1.246093 0.028293 2.5% 1003.0 jacobiACC_COPY@li.382										
 3.3% 0.850054 15066.0 MPI										
3.1% 0.800047 0.090590 11.6% 3009.0 mpi_waitall_										
Number of accelerators used: 8 of 8										
End Observations										
Wallclock time: 26.309761s										
himeno_mpi.ap2 (1,600 events in 0.444s)										

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Call Tree with GPU regions



Example Accelerator Statistics

				-				Copy Ca Out		Calltree PE=HIDE
TTME								ytes)		
	08	2.75	0	2.015	2	812.760		13.568	103	3 Total
				2.015				13.568		03 lbm3d2p d
	I		I		1	I		I		lbm3d2p_dACC_DATA_REGION@li.104
 	63.5%	1	 .747	1.74	7	2799.192	2			1 lbm3d2p_dACC_COPY@li.104
										36 streaming_
				0.0						27 streaming exchange
5		1		1		L	1		1	streaming_exchangeACC_DATA_REGION@1i.526
5	18.8%	1	0.517	1			- 1		1	1 streaming_exchangeACC_DATA_REGION@li.526(exclusiv
1	1.6%	1	0.043	0.0	42		- 1		1	9 streaming .ACC DATA REGION@li.907
5111	1.1%	1	0.031	0.0	31		- 1		1	4 streamingACC_REGION@li.909
							- 1		I	1 streamingACC_REGION@li.909(exclusive)
=										

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Example Kernel Statistics

	Avg	1	Avg	1	Avg	1	Avg	T.	Avg	Т	Avg		Function
G	rid	T.	Grid	1	Grid	1	Block	T.	Block	T.	Block		l de la constante de la constan
	х	T.	Y	1	Z	1	X Dim	T.	Y Dim	T.	Z Dim		l de la companya de l
1	Dim	T.	Dim	1	Dim	1		T.		T.			l de la companya de l
-													
	62163	3		1	l	1	1024	- 1	1		l	1	<pre> streamingACC_KERNEL@li.909</pre>
	402	2		1	I	1	128	- 1	1		I	1	grad_exchangeACC_KERNEL@li.443
	402	2		1	I	1	128	- 1	1		l	1	<pre> grad_exchangeACC_KERNEL@li.467</pre>
	402	2		1	I	1	128		1		I	1	grad_exchangeACC_KERNEL@li.476
	402	2		1	I	1	128	- 1	1		l	1	grad_exchangeACC_KERNEL@li.500
	400)		1	I	1	512		1		l	1	cal_velocityACC_KERNEL@li.1126
	400)		1	I	1	512		1		I	1	collisionaACC_KERNEL@li.474
	400)		1	l –	1	128	- 1	1		I	1	collisionbACC_KERNEL@li.597
	400)		1	l –	1	128	- 1	1		I	1	<pre> wall_boundaryACC_KERNEL@li.973</pre>
	400)		1	I	1	128	- 1	1		l	1	collisionbACC_KERNEL@li.629
	400)		1	I	1	512	- 1	1		l	1	recolorACC_KERNEL@li.823
	128	3		1	I	1	64	- 1	1		l	1	injectionACC_KERNEL@li.1281
	128	3		1	I	1	128		1		I	1	<pre> streaming_exchangeACC_KERNEL@li.829</pre>
	128	3		1	I	1	128		1		I	1	<pre> streaming_exchangeACC_KERNEL@li.729</pre>
	128	3		1	I	1	128		1		I	1	<pre> streaming_exchangeACC_KERNEL@li.641</pre>
	128	3		1	I	1	128		1		I	1	<pre> streaming_exchangeACC_KERNEL@li.538</pre>
	10:	LI		1	I	1	128		1		I	1	collisionbACC_KERNEL@li.612
	10:	LI		1	I	1	128		1		I	1	<pre> set_boundary_micro_pressACC_KERNEL@li.299</pre>
	10:	LI		1	I	1	128		1		I	1	set_boundary_macro_press2ACC_KERNEL@li.25
	14	L		1	1	1	256		1			1	streaming .ACC KERNEL@li.919

Questions ?

RAY

Files Generated and the Naming Convention

File Suffix	Description	08
a.out+pat	Program instrumented for data collection	
a.outs.xf	Raw data for sampling experiment, available after application execution	
a.outt.xf	Raw data for trace (summarized or full) experiment, available after application execution	
a.outst.ap2	Processed data, generated by pat_report, contains application symbol information	
a.outs.apa	Automatic profiling pnalysis template, generated by pat_report (based on pat_build –O apa experiment)	
a.out+apa	Program instrumented using .apa file	
MPICH_RANK_ORDER.Custom	Rank reorder file generated by pat_report from automatic grid detection an reorder suggestions	

GPU HW Performance Counters

<u>A7</u>

Accelerator Hardware Performance Counters

- Enable collection similarly to CPU counter collection:
 - CPU: PAT_RT_HWPC=group or events
 - GPU: PAT_RT_ACCPC=group or events

Enabling GPU counters causes change in behavior of application:

- Host needs to synchronize with the accelerator at each event (since accelerator executes asynchronously with the host)
- Can be seen through accelerator table
 - No counters: time spent waiting for kernel to complete is shown with ACC_SYNC_WAIT (a synchronization created by the compiler)
 - Counters: perftools syncs with accelerator with each event so Host Time is exclusive time for the containing region (since waiting occurs within the event's trace point instead of in the compiler sync)

Accelerator HW Counter Groups

- A predefined set of groups has been created for ease of use
 - Combines events that can be counted together
- ACCPC groups start at 1000, and will be incremented by 100 as new families of accelerators are supported
- Specify group by number or name
 - PAT_RT_ACCPC=1000 OR
 - PAT_RT_ACCPC=inst_exec_gst
- See accpc(5) and accpc_k20(5) man pages for list of groups and their descriptions